

HVL147

Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G0393-0300 Rev.3.00 Jan 13, 2006

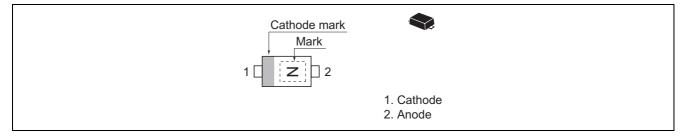
Features

- Adopting the trench structure improves low capacitance. (C = 0.31 pF max)
- Low forward resistance. (rf = 1.5Ω max)
- Low operation current.
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HVL147	Ν	EFP	PXSF0002ZA-A

Pin Arrangement





Absolute Maximum Ratings

		$(Ta = 25^{\circ}C)$		
Item	Symbol	Value	Unit	
Reverse voltage	V _R	30	V	
Forward current	I _F	100	mA	
Power dissipation	Pd	100	mW	
Junction temperature	Тј	125	°C	
Storage temperature	Tstg	-55 to +125	°C	

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

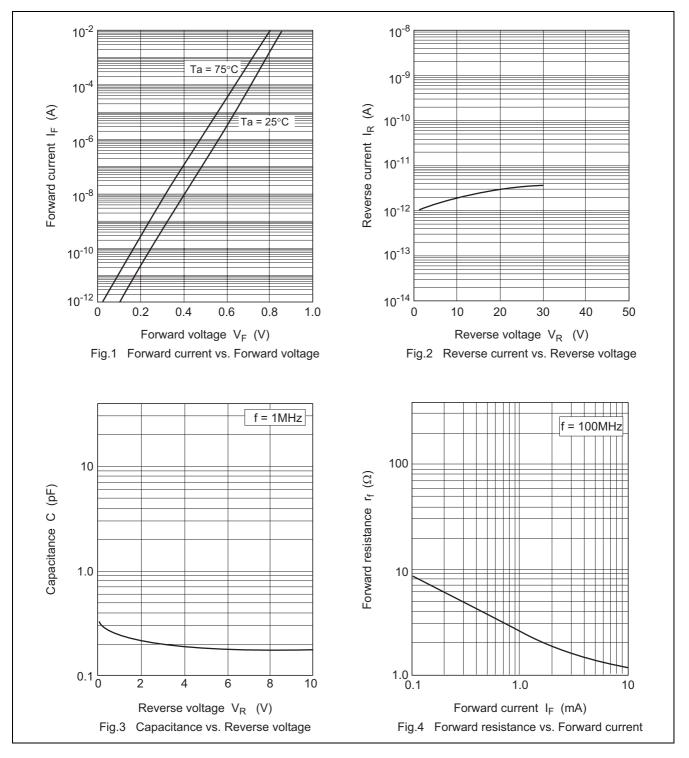
nA	1/ 001/
	$V_R = 30 V$
V	I _F = 10 mA
pF	$V_R = 1 V$, f = 1 MHz
Ω	I _F = 2 mA, f = 100 MHz
	I _F = 10 mA, f = 100 MHz
V	C = 200 pF, R = 0 Ω , Both forward and reverse direction 1 pulse.
	pF

Notes: 1. Failure criterion ; $I_R > 100 \text{ nA}$ at $V_R = 30 \text{ V}$

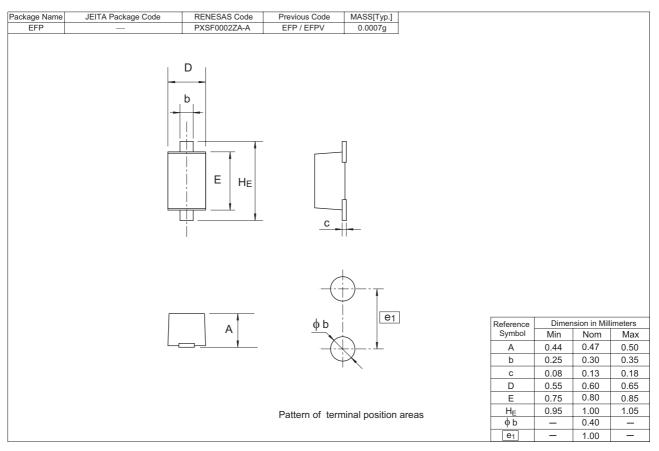
2. For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



Main Characteristic



Package Dimensions





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Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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